MITS

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE

(Deemed to be University under section 3 of UGC Act, 1956)

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CIRCULAR

Date: 13.11.2026
Madanapalle

All the **B.Tech. II Year I Semester (R25)** Regular students are hereby informed that the Model Lab Test will be conducted as per the time table given below.

TIME TABLE (Model Lab Test)

Dept. of Electronics & Communication Engineering

(2025 Admitted Batch)

Section	Course Name & Code	Date	Timings	Roll No.S	Venue	No.of Students
A	Electronic Devices and Circuits Laboratory - 25BAECELC01	24.11.2026	01.10 PM to 03.10 PM	25MRA04001 - 4015, 25MRA04017 -4022, 25MRA04024 to 4029, 25MRA04031- 4034, 25MRA04036 to 4040	LB 207	36
		27.11.2026	01.10 PM to 03.10 PM	25MRA04041, 25MRA04043 - 4055, 26MLA04001 to 4022		36
B	Electronic Devices and Circuits Laboratory - 25BAECELC01	24.11.2026	09.30 AM to 11.30 AM	25MRA04056, 25MRA04059 - 4064, 25MRA04066 - 4087, 25MRA04089 - 4095	LB 207	36
		27.11.2026	03.00 PM to 05.00 PM	25MRA04096 - 4100, 25MRA04102 - 4104, 26MLA04023 to 4026	LB 207	12
		20.11.2026	03.00 PM to 05.00 PM	26MLA04027 - 4050	LB 207	24
				Total No.of Students		**144**

Section	Course Name & Code	Date	Timings	Roll No.S	Venue	No.of Students
A	Digital Systems Laboratory using HDL - 25BAECELC02	25.11.2026	10.10 AM to 12.10 PM	25MRA04001 - 4015, 25MRA04017 -4022, 25MRA04024 to 4029, 25MRA04031- 4034, 25MRA04036 to 4040	LB 103	36
		24.11.2026	01.10 PM to 03.10 PM	25MRA04041, 25MRA04043 - 4055, 26MLA04001 to 4022	LB 103	36

B	Digital Systems Laboratory using HDL - 25BAECELC02	27.11.2026	02.00 PM to 04.00 PM	25MRA04056, 25MRA04059 - 4064, 25MRA04066 - 4087, 25MRA04089 - 4095	LB 103	36
		24.11.2026	09.30 AM to 11.30 AM	25MRA04096 - 4100, 25MRA04102 - 4104, 26MLA04023 to 4026	LB 103	12
		17.11.2026	09.30 AM to 11.30 AM	26MLA04027 - 4050	LB 103	24
				Total No.of Students		**144**

Section	Course Name & Code	Date	Timings	Roll No.S	Venue	No.of Students
A	Circuits and Simulation Laboratory - 25BAECELC03	27.11.2026	01.10 PM to 03.10 PM	25MRA04001 - 4015, 25MRA04017 -4022, 25MRA04024 to 4029, 25MRA04031- 4034, 25MRA04036 to 4040	LB 202	36
		25.11.2026	10.10 AM to 12.10 PM	25MRA04041, 25MRA04043 - 4055, 26MLA04001 to 4022	LB 202	36
B	Circuits and Simulation Laboratory - 25BAECELC03	25.11.2026	02.00 PM to 04.00 PM	25MRA04056, 25MRA04059 - 4064, 25MRA04066 - 4087, 25MRA04089 - 4095	LB 202	36
		23.11.2026	09.30 AM to 11.30 AM	25MRA04096 - 4100, 25MRA04102 - 4104, 26MLA04023 to 4026	LB 202	12
		16.11.2026	09.30 AM to 11.30 AM	26MLA04027 - 4050	LB 202	24
				Total No.of Students		**144**

Controller of Examinations

CONTROLLER OF EXAMINATIONS
Madanapalle Institute of Technology & Science
(Deemed to be University)
MADANAPALLE-517 325, A.P.